



IFW

PATENT  
Customer No. 22,852  
Attorney Docket No. 04329.3116

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:	)	
	)	
Masahisa SONODA et al.	)	Group Art Unit: 2811
	)	
Serial No.: 10/642,666	)	Examiner: Loke, Steven Ho Yin
	)	
Filed: August 19, 2003	)	
	)	Confirmation No.: 3478
For: NONVOLATILE SEMICONDUCTOR	)	
MEMORY DEVICE HAVING GROOVES	)	
ISOLATING THE FLOATING	)	
ELECTRODES OF MEMORY CELLS	)	
AND METHOD OF MANUFACTURING	)	
THE NONVOLATILE	)	
SEMICONDUCTOR MEMORY DEVICE	)	

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**RESPONSE TO RESTRICTION REQUIREMENT**

In the Office Action dated September 3, 2004, the period for response to which extends through October 3, 2004, the Examiner required restriction under 35 U.S.C.

§ 121 between the following groups:

Group I, claims 1-13, characterized by the Examiner as drawn to a semiconductor device; and

Group II, claims 14-22, characterized by the Examiner as drawn to a method to make a semiconductor device.

Applicants provisionally elect to prosecute Group I, including claims 1-13, without traverse.

Please grant any extensions of time required to enter this response and charge any additional required fees to our deposit account 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

Dated: September 29, 2004

By: 

\_\_\_\_\_  
Qingyu Yin\*

\*With limited recognition under 37 C.F.R. § 10.9(b)